

## 1W isolated DC-DC converter

Fixed input voltage, unregulated single output





# FEATURES

- Continuous short-circuit protection
- No-load input current as low as 12mA
- Operating ambient temperature range: -40  $^\circ C$  to +105  $^\circ C$
- High efficiency up to 84%
- Compact SMD package
- I/O isolation test voltage 3k VDC
- Industry standard pin-out

SF03\_XT-1WR3 series are designed for use in distributed power supply systems and especially suitable in applications such as pure digital circuits, low frequency analog circuits, relay-driven circuits and data switching circuits.

### Selection Guide

		Input Voltage (VDC)	C	utput	Full Load	Capacitive
Certification		Efficiency (%) Min./Typ.	Load(µF) Max.			
	SF0303XT-1WR3		3.3	303/30	73/77	2400
	SF0305XT-1WR3		5	200/20	78/82	2400
	SF0309XT-1WR3	3.3	9	111/11	80/84	1000
	SF0312XT-1WR3	(2.97-3.63)	12	83/8	80/84	560
	SF0315XT-1WR3		15	67/7	80/84	560
	SF0324XT-1WR3		24	42/4	80/84	220

Input Specifications						
Item	Operating Co	nditions	Min.	Тур.	Max.	Unit
Input Current 3 3\/DC input 5\/DC output 370/12 38		3.3VDC output		394/12	416/	
	389/					
(full load / no-load)		9VDC/12VDC/15VDC/24VDC output		361/12	379/	mA
Reflected Ripple Current*				30		
Surge Voltage (1sec. max.)			-0.7		5	VDC
Input Filter				Capacit	ance filter	
Hot Plug				Unav	ailable	
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Note: \*Reflected ripple current testing method please refer to *DC-DC Converter Application Note* for specific operation.

Item	Operating Condition	ons	Min.	Typ.	Max.	Unit
Voltage Accuracy			See	output regula	tion curve (F	ig. 1)
	Input voltage	3.3VDC output			±1.5	
Linear Regulation	change: ±1%	5VDC/9VDC/12VDC/15VDC/ 24VDC output		±1.2 15 20		
		3.3VDC output		15	20	
La sud Da sudadia s	5VDC output 10 15	15	01			
Load Regulation	10%-100% load	9VDC/12VDC/15VDC output		8	15	%
		24VDC output		6	15	
Ripple & Noise*	20MHz bandwidth			50	100	mVp-p
Temperature Coefficient	Full load			±0.02		<b>%/</b> ℃
Short-circuit Protection				Continuous,	self-recovery	/

# DC/DC Converter SF03\_XT-1WR3 Series

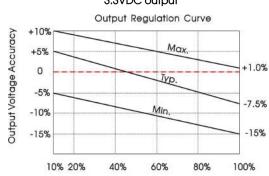


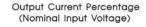
<b>General Specification</b>	IS				
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation	Input-output electric strength test for 1 minute with a leakage current of 1mA max.	3000			VDC
Insulation Resistance	Input-output resistance at 500VDC	1000			MΩ
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V		20		pF
Operating Temperature	Derating when operating temperature ${\geq}85^\circ\!\!\mathbb{C}$ , (see Fig. 2)	-40		105	
Storage Temperature		-55		125	°C
Case Temperature Rise	<b>Τα=25</b> ℃		25		
Storage Humidity	Non-condensing	5		95	%RH
Reflow Soldering Temperature*		Peak te	mp. Tc≤245° time≤60s	C <b>, maximum (</b> over 217℃	duration
Vibration		10-150	)Hz, 5G, 0.75m	nm. along X, Y	' and Z
Switching Frequency	Full load, nominal input voltage		220		kHz
MTBF	MIL-HDBK-217F@25°C	3500			k hours
Moisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020D.1		Lev	vel 1	
Note: * See also IPC/JEDEC J-STD-020	)D.1.				

Mechanical Specifica	lechanical Specifications	
Case Material	Black plastic; flame-retardant and heat-resistant (UL94 V-0)	
Dimensions	13.20 x 11.40 x 7.25 mm	
Weight	1.4g(Typ.)	
Cooling Method	Free air convection	

Electroma	gnetic Con	npatibility (EM	C)	
Emissions	CE	CISPR32/EN55032	CLASS B (see Fig. 4 for recommended circuit)	
ETTISSIOTIS	RE	CISPR32/EN55032	CLASS B (see Fig. 4 for recommended circuit)	
Immunity	ESD	IEC/EN61000-4-2	Air ±8kV, Contact ±6kV	perf. Criteria B

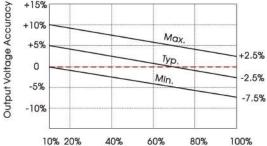
#### Typical Characteristic Curves 3.3VDC output



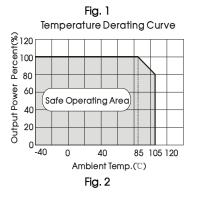




5VDC/9VDC/12VDC/15VDC/24VDC output

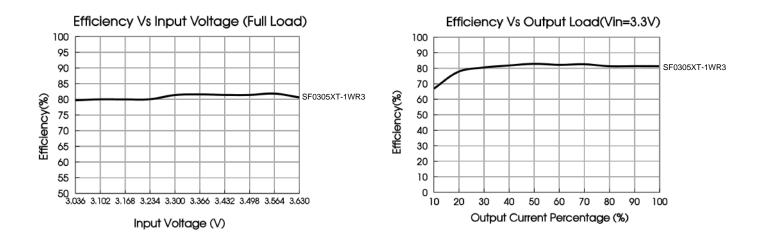


Output Current Percentage (Nominal Input Voltage)



Schmid Multitech GmbH Weinbergstraße 60b, 93105 Tegernheim - Germany www.schmid-m.com The Copyright and authority for the interpretation of the products are reserved by SCHMID-M. Specifications subject to change without notice.





### Design Reference

#### 1. Typical application

Input and/or output ripple can be further reduced, by connecting a filter capacitor from the input and/or output terminals to ground as shown in Fig.3.

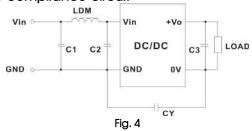
Choosing suitable filter capacitor values is very important for a smooth operation of the modules, particularly to avoid start-up problems caused by capacitor values that are too high. For recommended input and output capacitor values refer to Table 1.



DC	DC	Cout - O +Vo
Fig.	3	

Vin	Cin	Vo	Cout
3.3VDC	4.7µF/16V	3.3VDC	10µF/16V
		5VDC	10µF/16V
		9VDC	4.7µF/16V
		12VDC	2.2µF/25V
		15VDC	1µF/25V
		24VDC	0.47µF/50V

#### 2. EMC compliance circuit

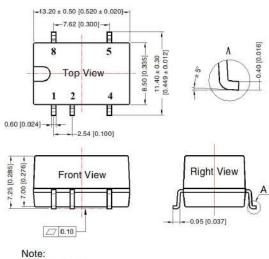


	C1, C2	4.7µF /16V
Emissions	C3	Refer to the Cout in Fig. 3
ETTISSIONS	CY	270pF/4kV
	LDM	6.8µH

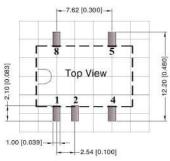


## Dimensions and Recommended Layout





Unit: mm[inch] Pin section tolerances:  $\pm 0.10[\pm 0.004]$ General tolerances:  $\pm 0.25[\pm 0.010]$ 

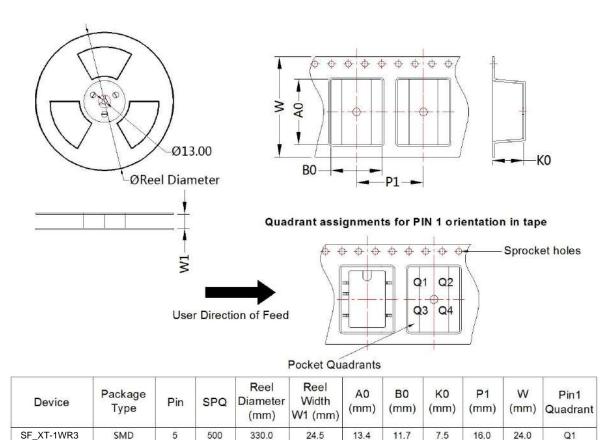


Note: Grid 2.54\*2.54mm

Pin-Out		
Pin	Mark	
1	GND	
2	Vin	
4	OV	
5	+Vo	
8	NC	

NC: Pin to be isolated from circuitry

### Tape and Reel Info





Notes:

- 1. If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet;
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on our company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.