DC/DC Converter SWRB_ST/SD-3WR2 Series



3W isolated DC-DC converter Wide input and regulated single output





FEATURES

- Ultra compact DIP/SMD package
- Wide 2:1 input voltage range
- Operating ambient temperature range: -40 $^{\circ}$ C ~ +85 $^{\circ}$ C
- I/O isolation test voltage: 1.5k VDC
- Short circuit protection (continuous)
- Industry standard pin-out
- Meets EN62368, UL62368 standards

SWRB_ST/SD-3WR2 series of isolated 3W DC-DC converter products with a 2:1 input voltage range. The product has a ultra-compact DIP/SMD package, operating temperature of -40°C to +85°C and continuous short circuit protection. The ultra-small volume design makes the converters an ideal solution for communications, instrumentation and industrial electronics applications.

Selection	Guide							
		Input Volta	ige (VDC)	Ou	itput	Ripple &	Full Load	Max.
Certification	Part No.	Nominal (Range)	Max. [®]	Voltage(VDC)	Current (mA) Max./Min.	Noise® (mVp-p) Typ./Max.	Efficiency (%) Min./Typ.	Capacitive Load(µF)
	SWRB1203ST/SD-3WR2			3.3	758/38		73/75	2700
	SWRB1205ST/SD-3WR2		20	5	600/30	50/100	77/79	2200
	SWRB1212ST/SD-3WR2	12 (9-18)		12	250/13		80/82	680
	SWRB1215ST/SD-3WR2	(7 10)		15	200/10		81/83	470
	SWRB1224ST/SD-3WR2			24	125/6		79/81	330
-	SWRB2403ST/SD-3WR2			3.3	758/38		72/74	2700
	SWRB2405ST/SD-3WR2			5	600/30		79/81	2200
	SWRB2412ST/SD-3WR2	24 (18-36)	40	12	250/13	1	81/83	680
	SWRB2415ST/SD-3WR2	(10 00)		15	200/10		81/83	470
	SWRB2424ST/SD-3WR2			24	125/6	†	81/83	330

Notes: ①Exceeding the maximum input voltage may cause permanent damage;

②Ripple & noise testing condition at nominal input voltage and 5%-100% load, the "tip and barrel" method is used for ripple and noise test, please refer to DC-DC Converter Application Notes for specific information.

Input Specifications						
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
	12VDC input voltage		314/30	338/50		
Input Current (full load/no-load)	24VDC input voltage	-	154/20	163/40	A	
Reflected Ripple Current	12VDC input voltage	-	40) mA		
кепестеа кірріе сапеті	24VDC input voltage	-	55			
Surgo Voltago (Isoo may)	12VDC input voltage	-0.7		25		
Surge Voltage (1sec. max.)	24VDC input voltage	-0.7		50	VDC	
Ctart up Valtage	12VDC input voltage	-		9	VDC	
Start-up Voltage	24VDC input voltage			18		
Input Filter	Capacitance filter					
Hot Plug		Unavailable				

Output Specifications						
Item	Operating Condition	ns	Min.	Тур.	Max.	Unit
Voltage Accuracy	5%-100% load, input vo	oltage range		±1	±3	
No load Output Voltage Accuracy	Input voltage range	3.3VDC output		±5	±7	
No-load Output Voltage Accuracy	Input voltage range	Others		±1.5	±5	%
Linear Regulation Input voltage variation from low to high load		n from low to high at full		±0.2	±0.5	

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DC/DC Converter

SWRB_ST/SD-3WR2 Series

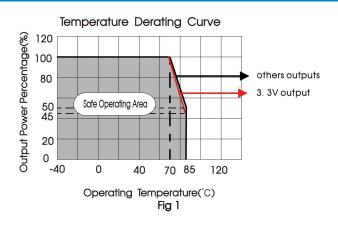
Load Regulation	5%-100% load	 ±0.5	±l	%
Transient Recovery Time	25% load stop obango	 1	3	ms
Transient Response Deviation	25% load step change	 ±2.5	±5	%
Temperature Coefficient	Full load	 	±0.03	%/℃
Short-circuit Protection		Continuous,	self-recovery	

General Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Isolation	Input-output Electric Strength test for 1 minute with a leakage current of 1mA max.	1500	_		VDC
Insulation Resistance	Input-output insulation at 500VDC	1000	-		MΩ
Isolation Capacitance	Input-output capacitance at 100KHz/0.1V		100		pF
Operating Temperature	See Fig. 1	-40		+85	$^{\circ}$
Storage Temperature		-55		+125	
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds	-		+300	℃
Reflow Soldering Temperature			oerature ≤24 °C. see also IP	-	
Storage Humidity	Non-condensing	-		95	%RH
Switching Frequency (PFM Mode)	Full load, nominal input voltage		300		KHz
MTBF	MIL-HDBK-217F@25℃	1000			K hours

Mechanical Specifications							
Case Material	Black flame-retardant and hea	Black flame-retardant and heat-resistant plastic					
Dimonsion	SWRB_SD-3WR2	14.00 x 14.00 x 9.00 mm					
Dimension	SWRB_ST-3WR2	15.00 x 14.00 x 9.10 mm					
Weight 2.2g(Typ.)							
Cooling Method	Free air convection	Free air convection					

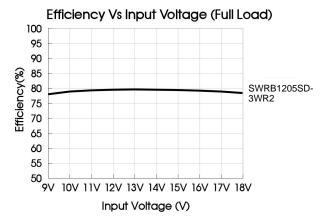
Electrom	agnetic Compo	atibility (EMC)		
Emissions	CE	CISPR32/EN55032	CLASS B (see Fig. 3-2) for recommended circuit)	
ETTISSIONS	RE	CISPR32/EN55032	CLASS B (see Fig. 3-2) for recommended circuit)	
	ESD	IEC/EN61000-4-2	Contact ±6KV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
Immunity	EFT	IEC/EN61000-4-4	±2KV (see Fig. 3-① for recommended circuit)	perf. Criteria B
	Surge	IEC/EN61000-4-5	line to line ±2KV (see Fig. 3-1) for recommended circuit)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A

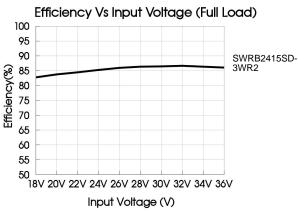
Product Characteristic Curve

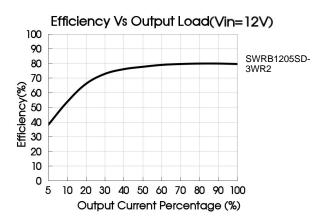


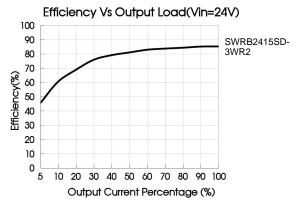
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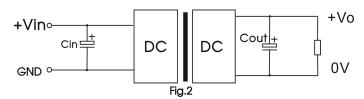


Design Reference

1. Recommended circuit

All the DC/DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 2.

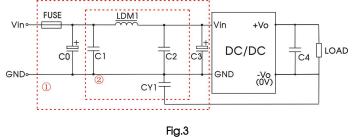
Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout, connecting a "Y" capacitor between input "GND" and output "0V", and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the max. capacitive load value of the product.



Parameter description:

Vin(VDC)	12	24
Cin	47uF/25V	47uF/50V
Vo(VDC)	3.3, 5	12, 15, 24
Cout	100uF/6.3V	27uF/35V

EMC compliance circuit



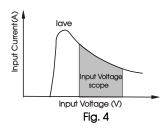
Parameter description:

Part No.		Vin:12VDC				Vir	า:24V	DC		
Vo (VDC)	3.3	5	12	15	24	3.3	5	12	15	24
FUSE	slow b	slow blow, choose accord			low blow, choose according to actual input current					rrent
C0		1000µF/25V				680µF/50V			0V	
C1	C1 10µF/50V		4	4.7µF/50V		10µF/50V		' Z	4.7µF/50V	
LDM1				15µH						
C2		4.7			4.7µF	/50V				
C3				330µF	μF/50V					
CY1	1nF,			1nF/	2KV					
C4			F	Refer	to the	Cout	Fig.2			

3. Input current

When the electricity is provided by the unstable power supply, please make sure that the range of the output voltage fluctuation and the ripple voltage of the power supply do not exceed the indicators of the modules. Input current of power supply should afford the flash startup current of this kind of DC/DC module(see Fig. 4).

Generally:Vin=12V series | lave =600mA Vin=24V series | lave =300mA

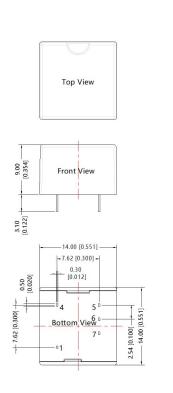


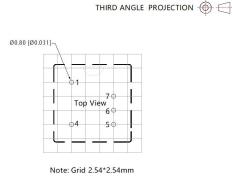
4. Output load requirements

When using, the minimum load of the module output should not be less than 5% of the nominal load. In order to meet the performance parameters of this datasheet, please connect a 5% dummy load in parallel at the output end, the dummy load is generally a resistor, please note that the resistor needs to be used in derating.

Dimensions and Recommended Layout

SWRB_SD-3WR2 series

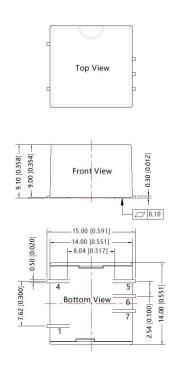


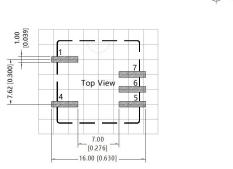


Pin-Out
Function
GND
Vin
+Vo
NC
0V

Note: Unit: mm[inch] Pin diameter tolerances: $\pm 0.10[\pm 0.004]$ General tolerances: $\pm 0.50[\pm 0.020]$

SWRB_ST-3WR2 series





THIRD ANGLE PROJECTION (

Note: Grid 2.54*2.54mm

Pin-Out					
Pin	Function				
1	GND				
4	Vin				
5	+Vo				
6	NC				
7	0V				

Note:

Unit: mm[inch]

Pin diameter tolerances: ±0.10[±0.004] General tolerances: ±0.50[±0.020]

Notes:

- 1. Recommend to use module with more than 5% load, if not, the ripple of the product may exceeds the specification, but does not affect the reliability of the product;
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated load;
- 4. All index testing methods in this datasheet are based on company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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